



7/ELECTION
3/4/03
Makuch
PATENT

Attorney Docket
33035 M 084

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Seiji YAEGASHI, et al. **CONFIRMATION NO. 7336**
Serial No. : 10/026,950 Art Unit : 2815
Filed : December 27, 2001 Examiner : Bradley W. BAUMEISTER
For : Heterojunction Bipolar Transistor and Method of Making
Heterojunction Bipolar Transistor

RESPONSE TO PAPER NO. 6

Commissioner for Patents
Washington, DC 20231

Sir :

A response to the Office Action of February 4, 2003 is due by March 4, 2003.

The Action required restriction between Group I of claims 15-20 drawn to an HBT, and Group II of claims 1-14 drawn to a method of making an HBT.

Applicants hereby elect Group II of claims 1-14 directed to a method of making an HBT for prosecution in this application. The election is made without traverse. Applicants reserve the right to file divisional applications for the non-elected claims in due course.

Applicants submit that this application now is in condition for examination on the merits and early action in that regard is solicited.

Respectfully submitted,

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Date : February 26, 2003

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